PAGE 3/3

NOV 0 9 2005

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09/21/2005

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Minneapolis, MN 55402

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Antonette Peters	(Depositor's wood
The state of the s	(Sign absra
Novvember 9, 2005	(Duto)

APPLICATION NO. PILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/010,273 12/05/2001 Hiroaki Aseda 10873.851US01

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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Change of correspondence address (or Change of Correspondence Address form FTO/SB/122) strached. D "Fee Address" indication (or "Fee Address" Indication form FTO/SB/1; Rev 03-02 or more recent) attached. Use of a Casteme Number is required.		Courespondence	or agents OR, alternatively,				Schumann,
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The following fee(s) ar I Issue Fee Poblication Fee (No Advance Order - # of Advance Director of the USPTU DIFF. The Issue Fee and durent as shown by the re- Authorized Signature Typed or printed name	e enclosed: small entity discount permitte of Copies s (frum status indicated above SMALL HNTTTY status. See) is requested to apply the last Publication Fee (if required) y ands of the United States Pate Douglast P. Mueller	db 37 CFR 1.27. In Fee and Publicate rill not be accepted at and Trademerk	A Payment of A check Payment Of the Direction Of the Direction Of the Direction Of the Direction Office.	Fee(s): in the amount of the fee(s) is on by credit card. Form PTO-2018 ctar is hereby sudhorized by cl ount Number 50-3478 ant is no longer claiming SMAI ny) or to re-epply any previously other than the applicant; a regi	ts attached. Is attached. LENTITY step paid issue for stered atterney. ember 9, 2 No. 30,304	ired fee(s), or close an extra co atms. See 37 Cl to the applica or agent; or th	credit any nyerpaymen opy of this form). FR 1.27(g)(2), tion identified above, assigned or other part







HAMRE, SCHUMANN,

AN INTERNATIONAL INTELLECTUAL PROPERTY LAW FIRM

FAX TRANSMISSION

November 9, 2005

TO:

Mail Stop: ISSUE FEE

Examiner: NGUYEN

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10873.0851US01

TELEPHONE: (612) 455.3800

Total pages, including cover letter:

PTO FAX NUMBER: 571.273.2885

If all pages are NOT received, please call us at 612.455.3800 or fax us at 612.455.3801.

Title of Document:

Issue Fee Transmittal - Part B, Communication regarding Title

Applicant:

ASADA

Serial No.: App. Filed:

10/010,273 December 5, 2001

Group Art No.: 2824

Conf. No.:

5636

Please charge Deposit Account No. 50-3478 in the amount of \$1400 for Issue Fee and \$300 for Publication Fee. Please charge any additional fees or credit overpayment to Deposit Account No. 50-3478. Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers, if appropriate.

By:

Name: Douglas P. Mueller

Reg. No.: 30,300

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

Antonette C. Peters

Signature

Date

225 SOUTH SIXTH STREET - SUITE 2650 - MINNEAPOLIS - MN 55402 TEL 612.455.3800 - FAX 612.455.3801 WWW.HSMLCOM - MAIL@HPMLCOM

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1/09/20**p**5 16:26 612.455.3801 NOV 0 9 2005

HSML, P.C. (acp)

PAGE 2/3

S/N 10/010,273

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

ASADA

Examiner:

NGUYEN

Serial No.:

10/010,273

Group Art Unit:

2824

Filed:

December 5, 2001

Docket No .:

10873.0851US01

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on November 9, 2005.

talinatur Name: Antonette C Peters

COMMUNICATION REGARDING TITLE

Mail Stop: ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Commissioner:

Applicants respectfully bring to the attention of the Examiner that the title which appears on the Notice of Allowance does not reflect the amendment to the title made on August 30, 2005. Please amend the title, according to this prior amendment, as follows:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE MEMORY DEVICE HAVING ADDRESS GENERATING CIRCUIT USING PHASE ADJUSTMENT BY SAMPLING DIVIDED CLOCK TO GENERATE ADDRESS SIGNAL OF SEVERAL BITS HAVING ONE BIT CHANGED IN SEQUENTIAL ORDER

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller (Reg. No. 30,300), at (612)455.3804.

53148

PATENT TRADEMARK OFFICE

Dated: November 9, 2005

Respectfully submitted,

HAMRE, SCHUMANN, MUELLER & LARSON, P.C. P.O. Box 2902-0902

Minneapolis, MN 55402-0903

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glas P. Mueller

Reg No. 30,300

DPM/acp